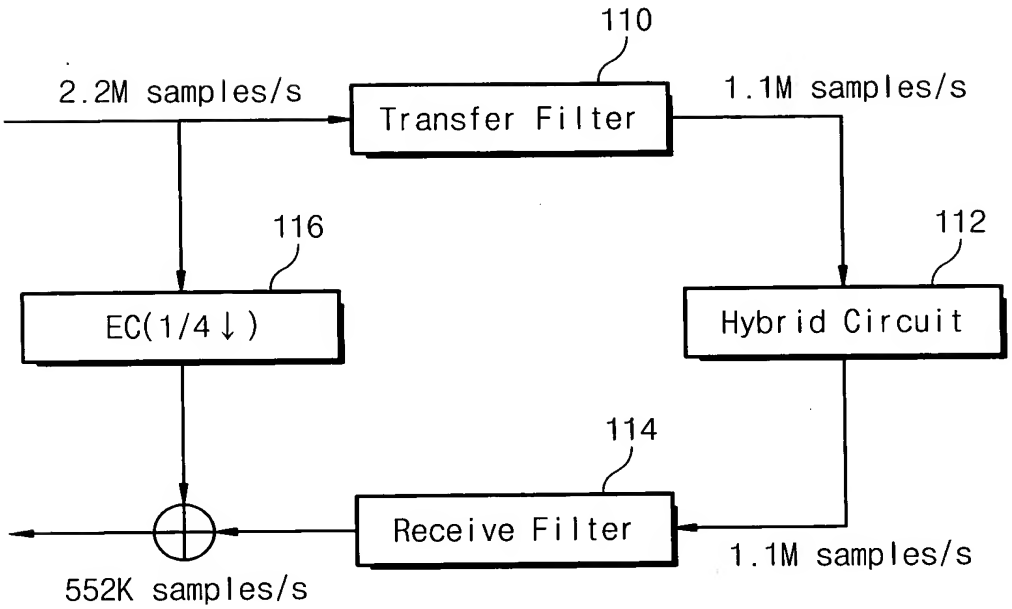
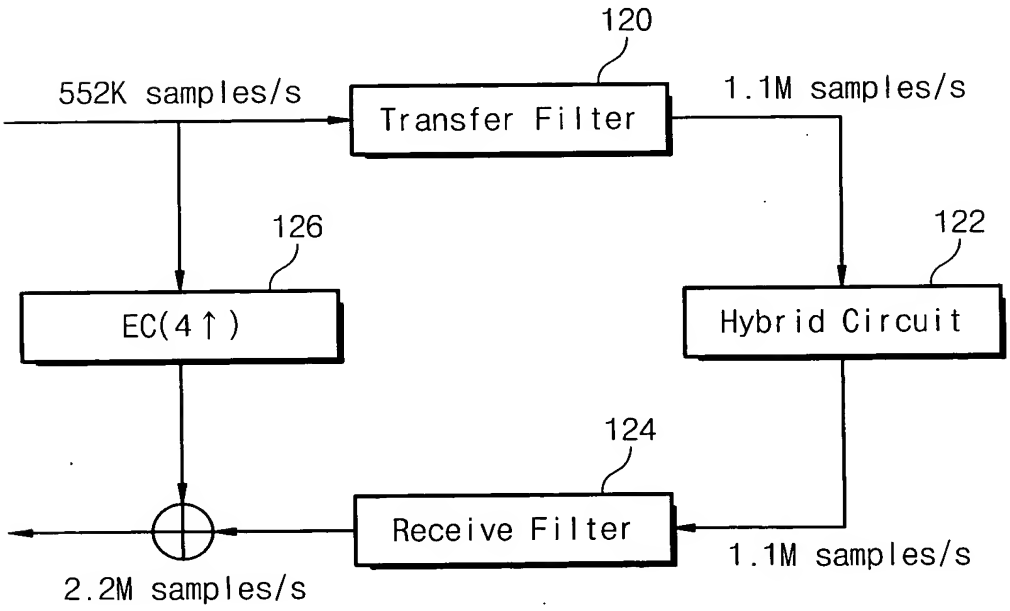
Fig. 1A



CO Mode

Fig. 1B



RT Mode

Fig. 2

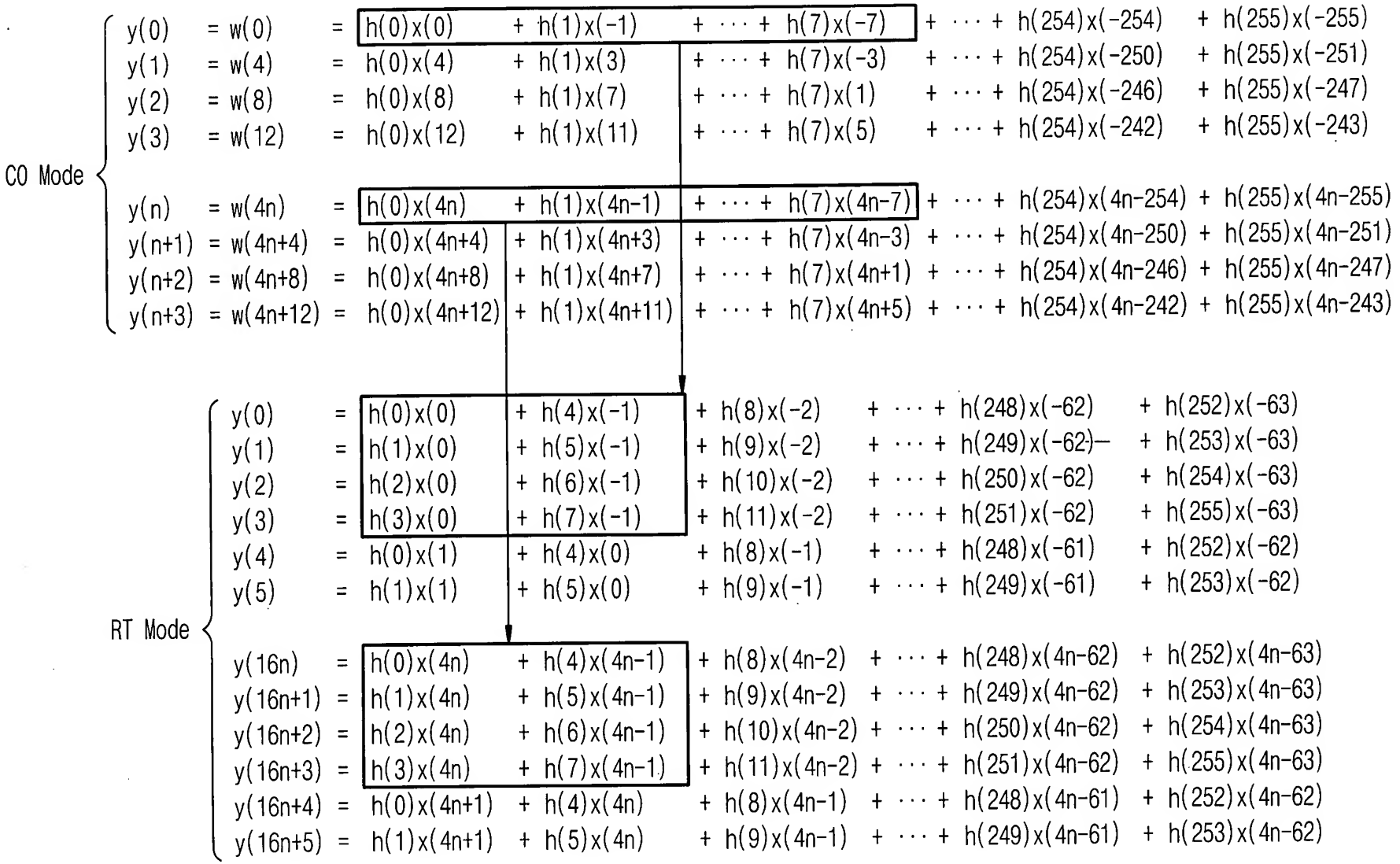

CO Mode
y(0) = w(0) = h(0)x(0) + h(1)x(-1) + ··· + h(7)x(-7) + ··· + h(254)x(-254) + h(255)x(-255)
y(1) = w(4) = h(0)x(4) + h(1)x(3) + ··· + h(7)x(-3) + ··· + h(254)x(-250) + h(255)x(-251)
y(2) = w(8) = h(0)x(8) + h(1)x(7) + ··· + h(7)x(1) + ··· + h(254)x(-246) + h(255)x(-247)
y(3) = w(12) = h(0)x(12) + h(1)x(11) + ··· + h(7)x(5) + ··· + h(254)x(-242) + h(255)x(-243)
y(n) = w(4n) = h(0)x(4n) + h(1)x(4n-1) + ··· + h(7)x(4n-7) + ··· + h(254)x(4n-254) + h(255)x(4n-255)
y(n+1) = w(4n+4) = h(0)x(4n+4) + h(1)x(4n+3) + ··· + h(7)x(4n-3) + ··· + h(254)x(4n-250) + h(255)x(4n-251)
y(n+2) = w(4n+8) = h(0)x(4n+8) + h(1)x(4n+7) + ··· + h(7)x(4n+1) + ··· + h(254)x(4n-246) + h(255)x(4n-247)
y(n+3) = w(4n+12) = h(0)x(4n+12) + h(1)x(4n+11) + ··· + h(7)x(4n+5) + ··· + h(254)x(4n-242) + h(255)x(4n-243)
RT Mode
y(0) = h(0)x(0) + h(4)x(-1) + h(8)x(-2) + ··· + h(248)x(-62) + h(252)x(-63)
y(1) = h(1)x(0) + h(5)x(-1) + h(9)x(-2) + ··· + h(249)x(-62) + h(253)x(-63)
y(2) = h(2)x(0) + h(6)x(-1) + h(10)x(-2) + ··· + h(250)x(-62) + h(254)x(-63)
y(3) = h(3)x(0) + h(7)x(-1) + h(11)x(-2) + ··· + h(251)x(-62) + h(255)x(-63)
y(4) = h(0)x(1) + h(4)x(0) + h(8)x(-1) + ··· + h(248)x(-61) + h(252)x(-62)
y(5) = h(1)x(1) + h(5)x(0) + h(9)x(-1) + ··· + h(249)x(-61) + h(253)x(-62)
y(16n) = h(0)x(4n) + h(4)x(4n-1) + h(8)x(4n-2) + ··· + h(248)x(4n-62) + h(252)x(4n-63)
y(16n+1) = h(1)x(4n) + h(5)x(4n-1) + h(9)x(4n-2) + ··· + h(249)x(4n-62) + h(253)x(4n-63)
y(16n+2) = h(2)x(4n) + h(6)x(4n-1) + h(10)x(4n-2) + ··· + h(250)x(4n-62) + h(254)x(4n-63)
y(16n+3) = h(3)x(4n) + h(7)x(4n-1) + h(11)x(4n-2) + ··· + h(251)x(4n-62) + h(255)x(4n-63)
y(16n+4) = h(0)x(4n+1) + h(4)x(4n) + h(8)x(4n-1) + ··· + h(248)x(4n-61) + h(252)x(4n-62)
y(16n+5) = h(1)x(4n+1) + h(5)x(4n) + h(9)x(4n-1) + ··· + h(249)x(4n-61) + h(253)x(4n-62)

Fig. 3

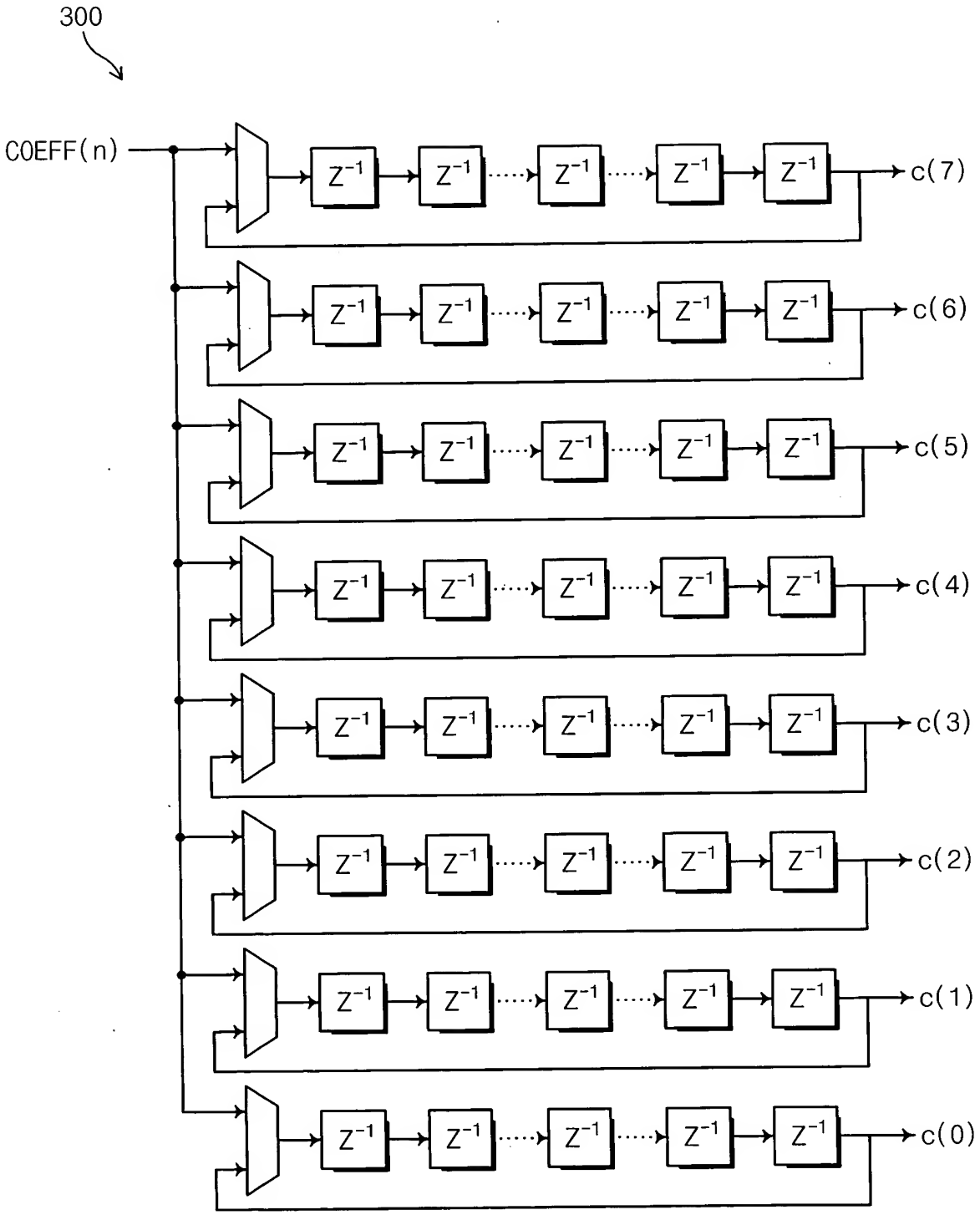
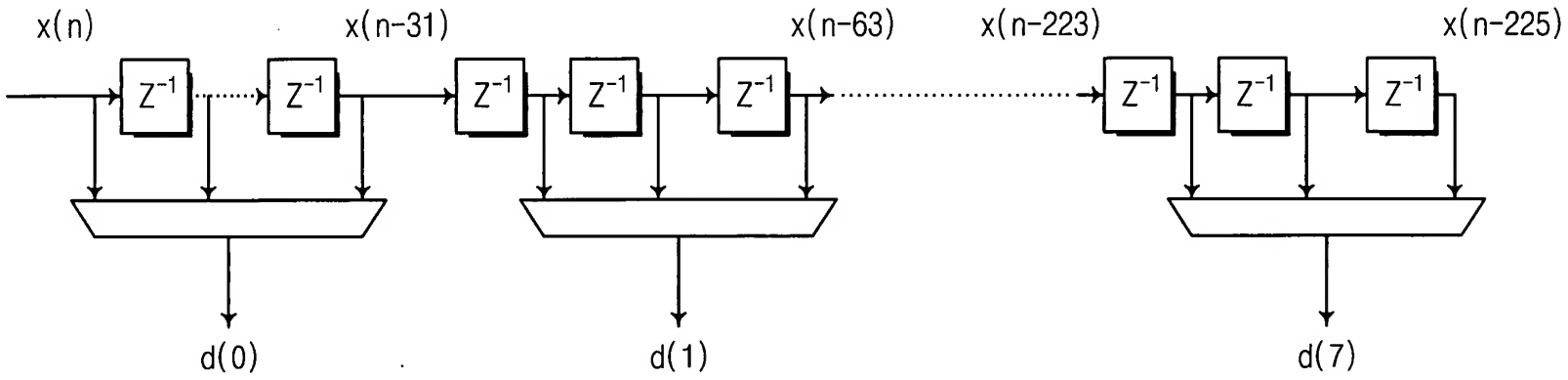
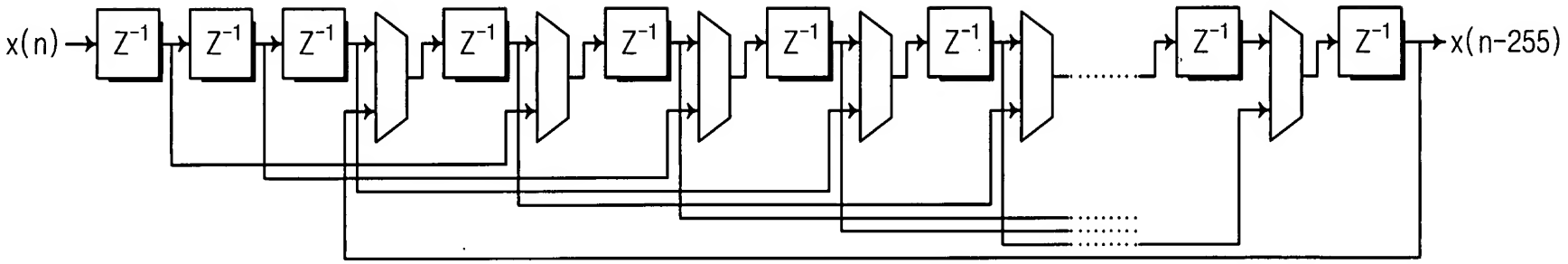


Fig. 4A



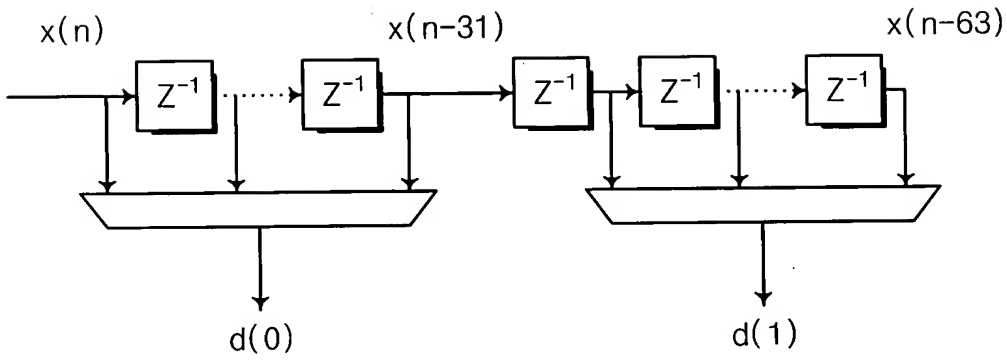
CO Mode

Fig. 4B



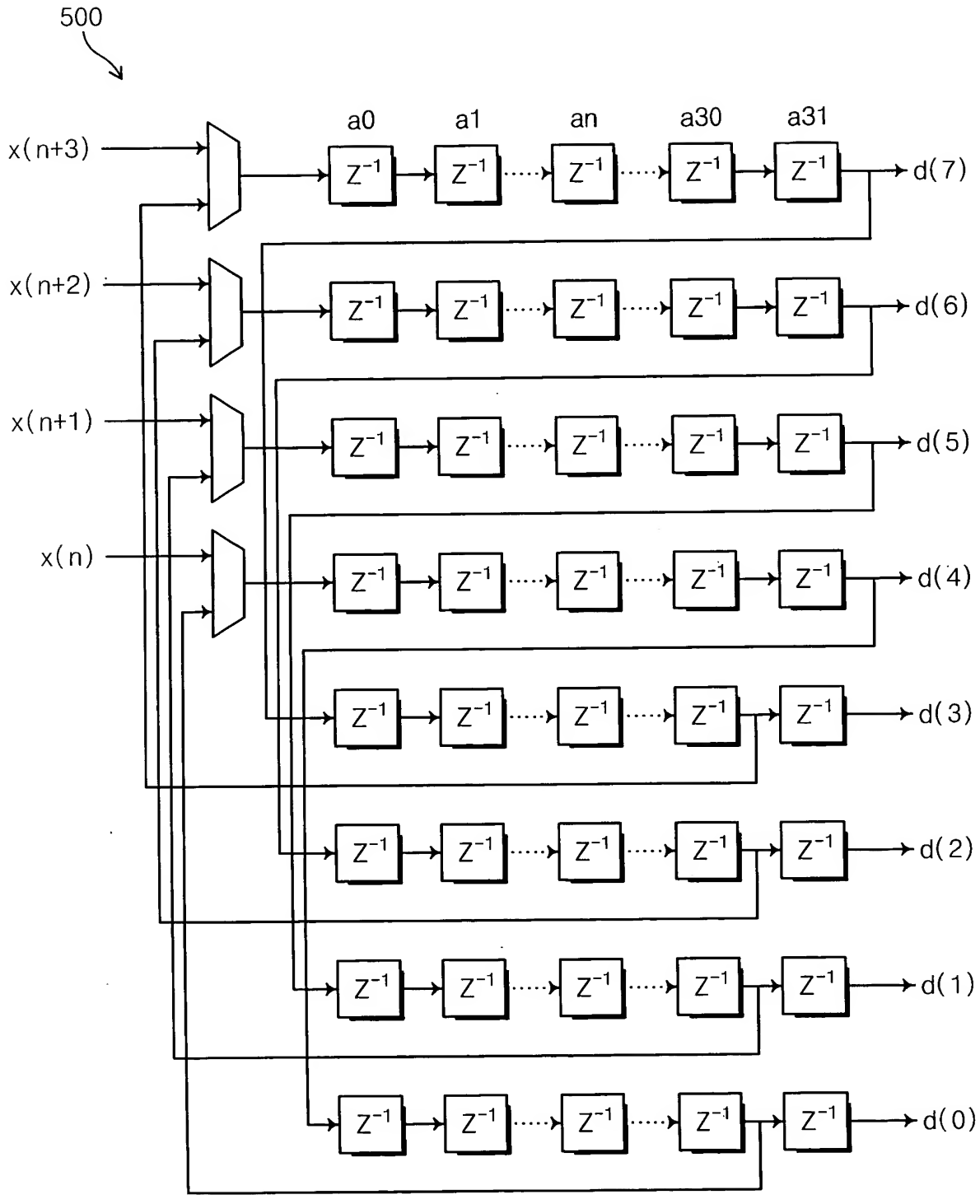
CO Mode

Fig. 4C



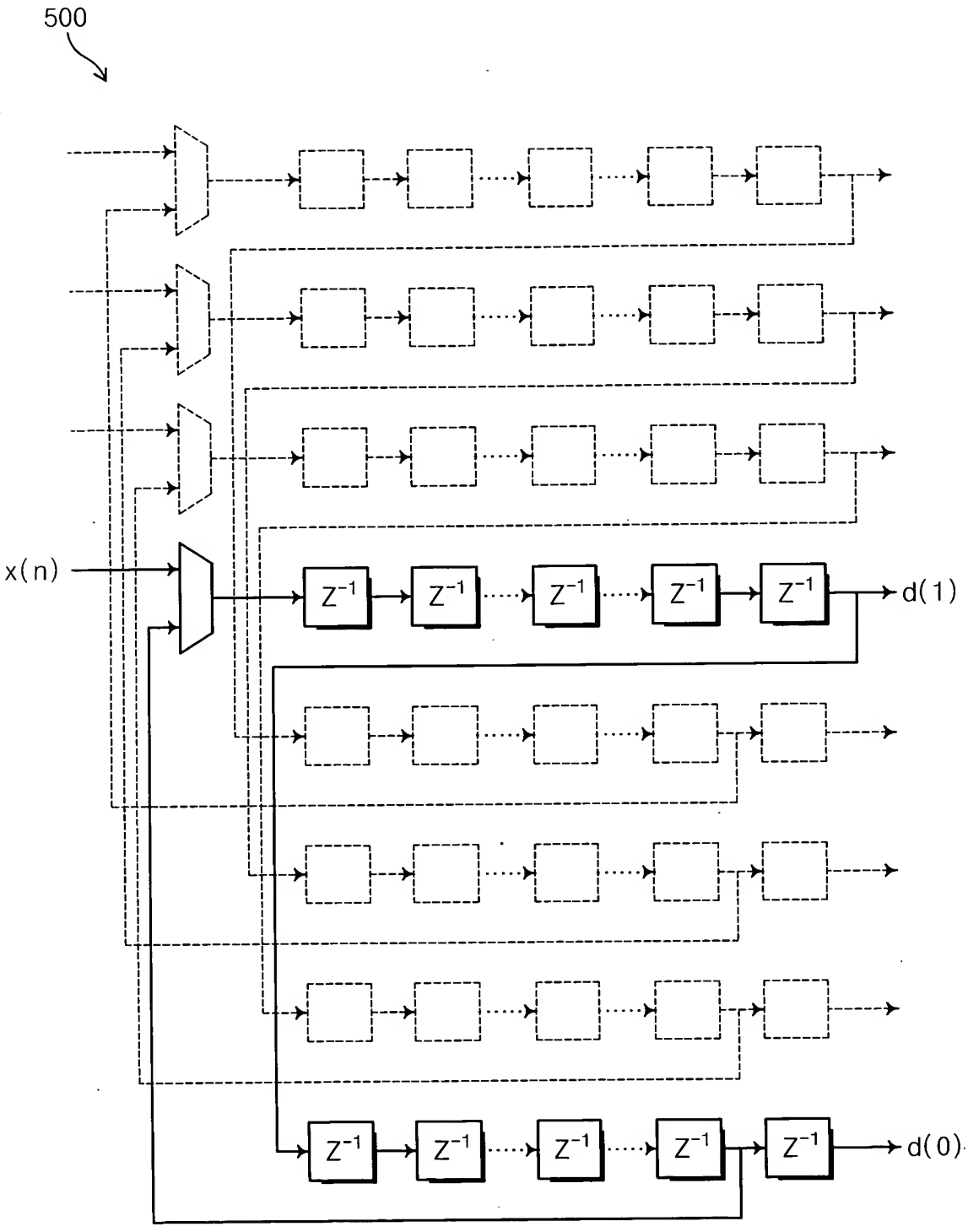
RT Mode

Fig. 5A



C0 Mode

Fig. 5B



RT Mode

Fig. 6

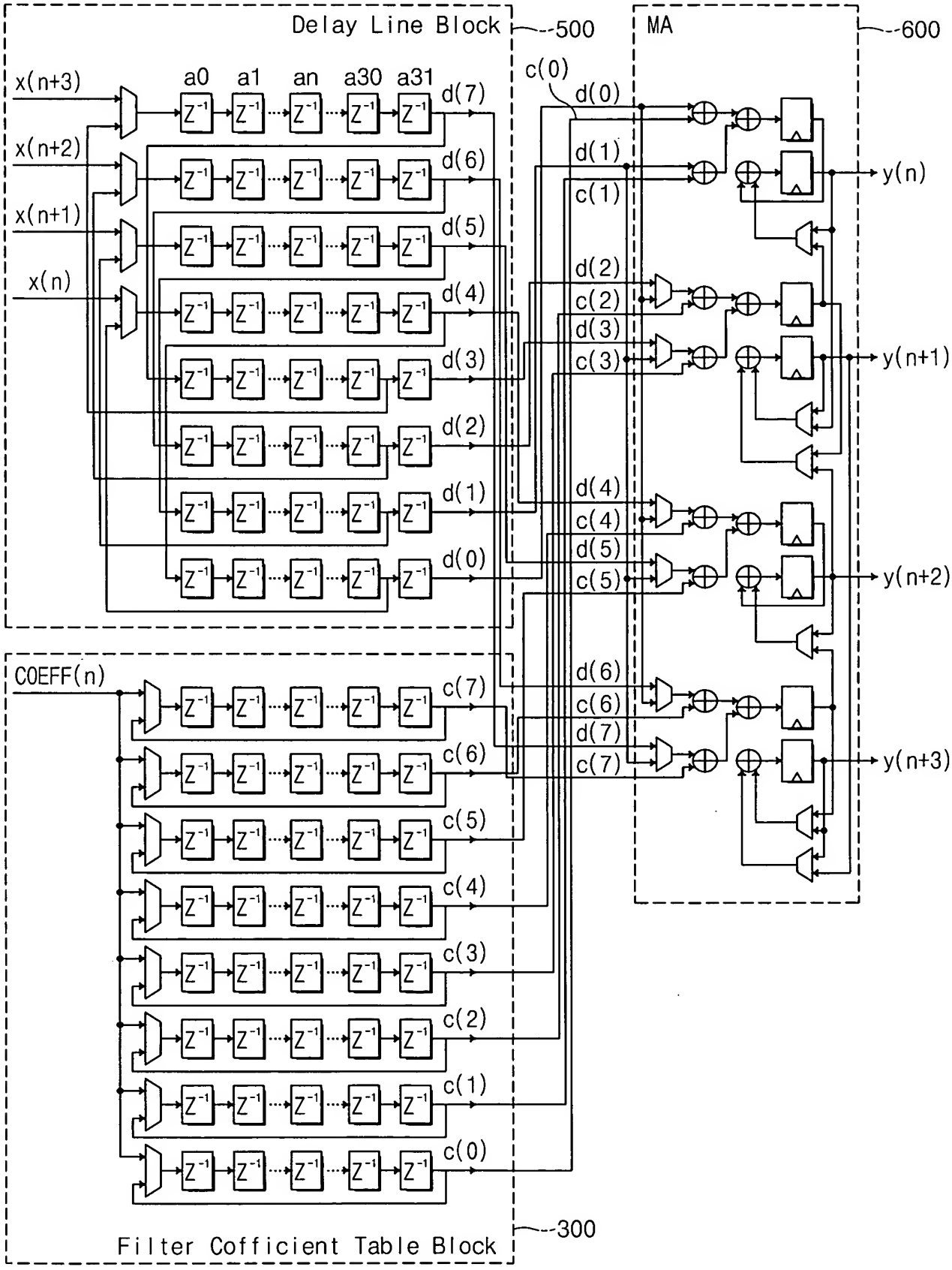
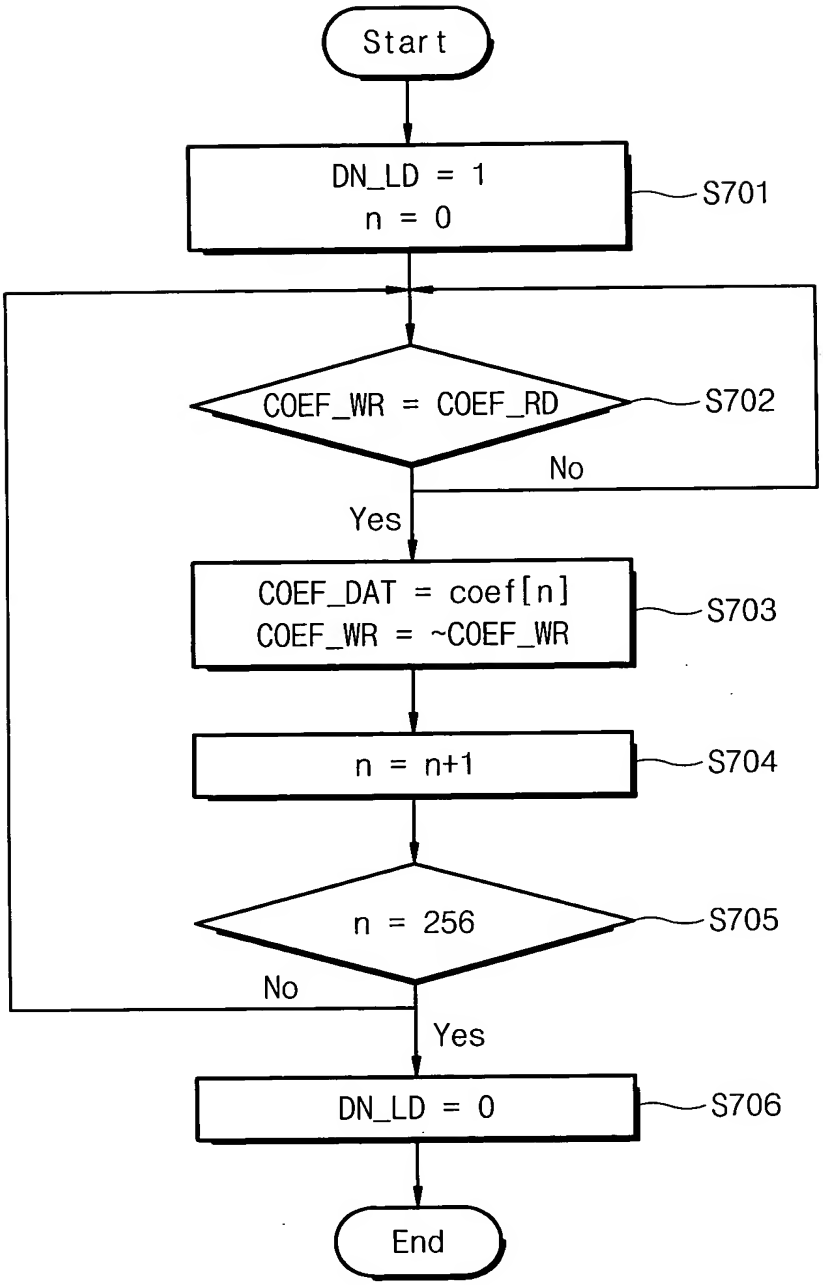
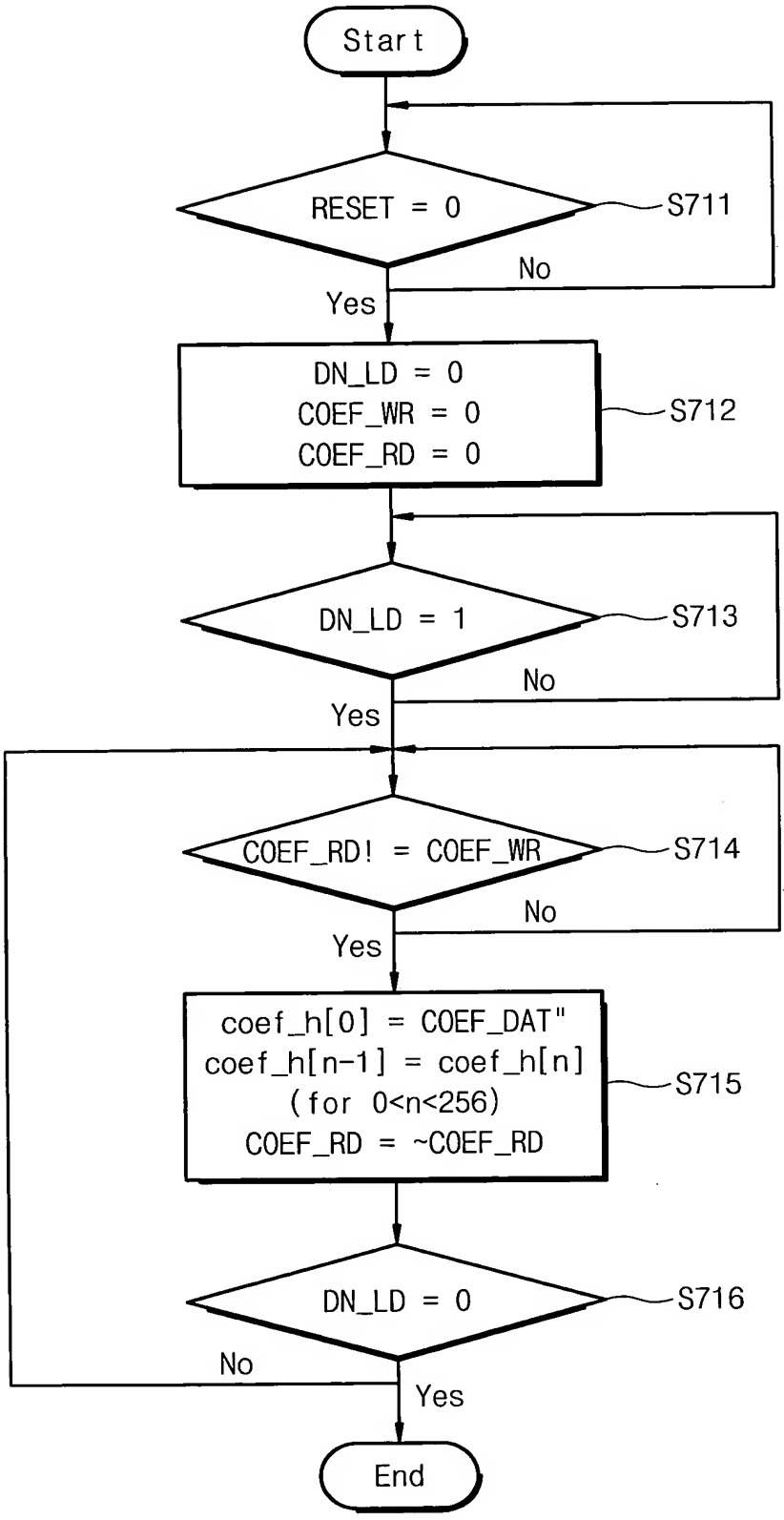

Delay Line Block
500
MA
600
x(n+3)
x(n+2)
x(n+1)
x(n)
a0
a1
an
a30
a31
d(7)
d(6)
d(5)
d(4)
d(3)
d(2)
d(1)
d(0)
c(0)
c(1)
c(2)
c(3)
c(4)
c(5)
c(6)
c(7)
y(n)
y(n+1)
y(n+2)
y(n+3)
COEFF(n)
Filter Cofficient Table Block
300

Fig. 7A



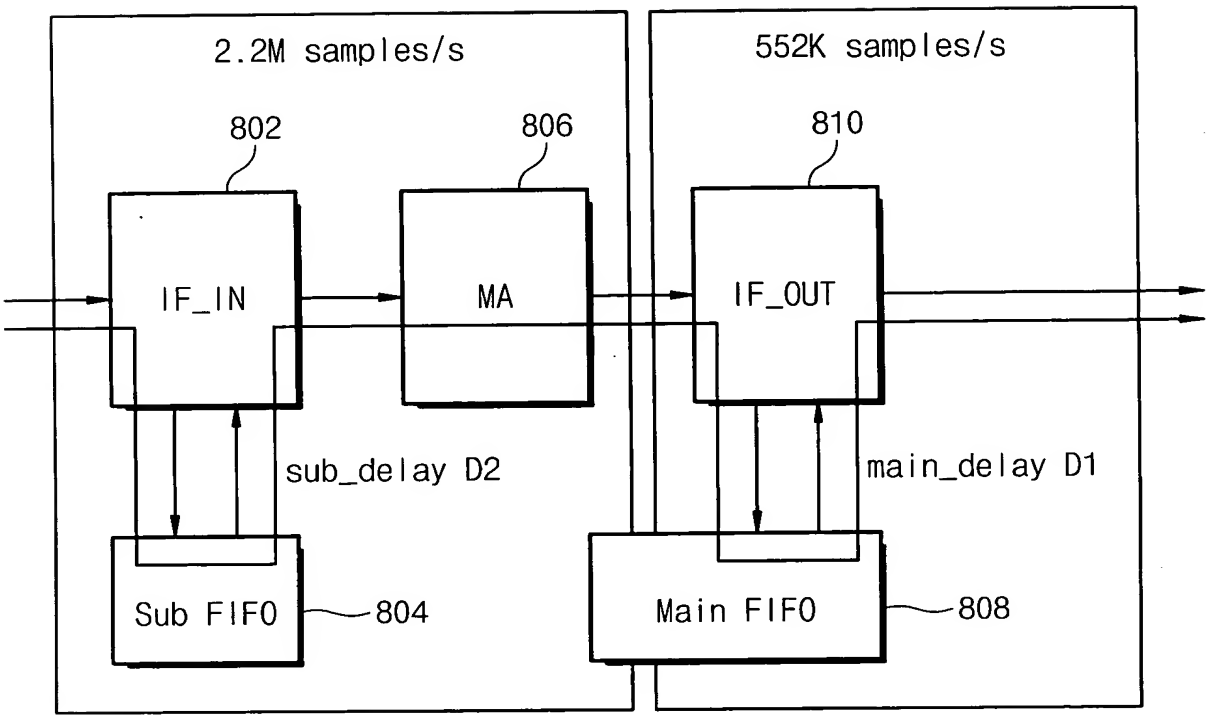
- DSP Operation -

Fig. 7B



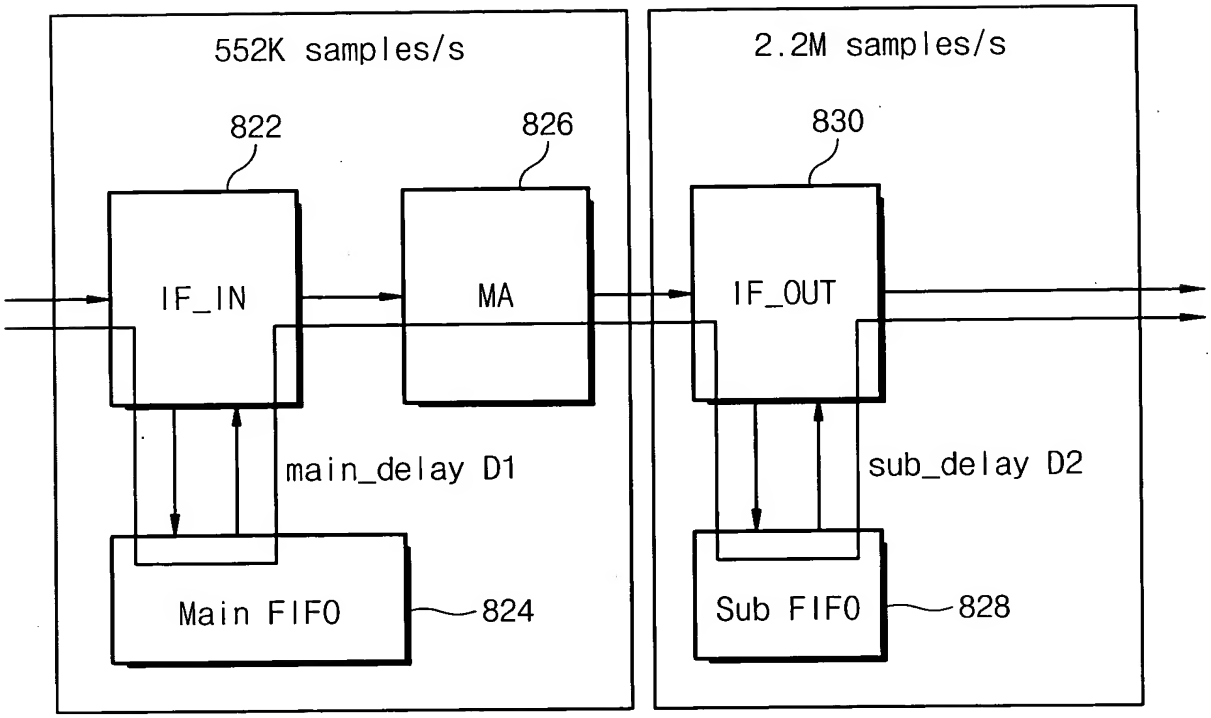
- EC H/W Operation -

Fig. 8A



C0 Mode

Fig. 8B



RT Mode